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(54) **SURFACE OPTICAL AMPLIFIER AND METHOD OF PRODUCING THE SAME**

(57) A surface-type light amplifier device has an active layer (13) of a light amplification section (11) sandwiched between an n-type semiconductor cladding layer (12) that is an n-type semiconductor layer and a p-type semiconductor multilayer reflecting mirror (14). The light amplification section is attached to a transparent substrate (21) on the side of the n-type semiconductor cladding layer. A plurality of divided electrodes (16) form electrical continuity relative to the p-type semiconductor multilayer reflecting mirror via a p-type cap layer (15) provided on the reflecting mirror. An electrode (18) forming electrical continuity relative to the n-type semiconductor cladding layer is connected to a wiring conductor (20) provided on the surface of the transparent substrate. The device enables amplification of a single, uniform, large-diameter light beam and oscillation of a laser.

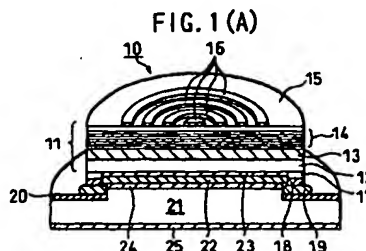
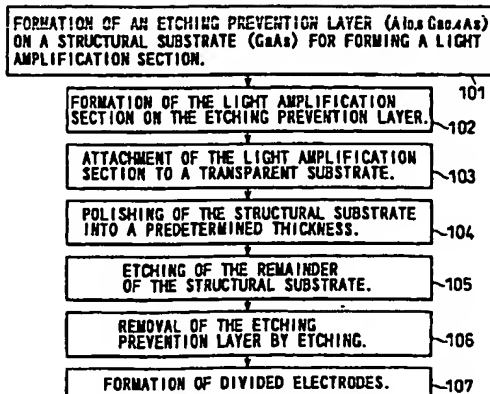


FIG. 1(B)



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Description

Field of Art:

[0001] The present invention relates to a surface-type light amplifier device usable as a surface light emitting laser etc. when a resonator is disposed outside the device and to a method for the manufacture thereof. The "surface-type" light amplifier device referred to herein is a device comprising a light function portion for amplifying and emitting light and a substrate for physically supporting the light amplification function portion, wherein the emitted light rises with a specific angle relative to the surface of the substrate, generally in the direction of intersecting the substrate surface at right angles (in the normal direction).

Background Art:

[0002] As for surface-type light amplifier devices of this type, there is the one disclosed in Reference Literature 1: "Electrically pumped mode-locked vertical-cavity semiconductor lasers" (W. Jiang, M. Shimizu, R. P. Mirin, T. E. Reynolds and J. E. Bowers, Optics Letters, Vol. 18, No. 22, pp. 1937-1939, 1993). As shown in Figure 2, the prior art surface-type light amplifier device 30 structurally comprises an n-type GaAs substrate 31 on which a multilayer reflecting mirror of n-type semiconductor 32, an n-type cladding layer 33, an n-type GaAs active layer 34, a p-type cladding layer 35, a p-type AlGaAs layer 37 and a p-type GaAs contact layer 38 are deposited in the order mentioned. The p-type AlGaAs layer 37 and p-type GaAs contact layer 38 are partially cut off into a shape having a predetermined surface area. An antireflection coating 39 is deposited onto the top surface of the p-type GaAs contact layer. A surface electrode 40 is formed on the p-type cladding layer 35, with an insulating film 36 sandwiched therebetween, so as to surround the cut-off portions and come into contact with the top peripheral part of the p-type GaAs contact layer 38. A substrate electrode 41 is deposited on the bottom surface of the n-type substrate 31.

[0003] Injection of carriers into the n-type GaAs active layer 34 is attained by current injection, i.e. by applying voltage between the surface electrode 40 and the substrate electrode 41.

[0004] Holes are injected from the surface electrode 40 into the n-type GaAs active layer 34 sequentially via the p-type GaAs contact layer 38, p-type AlGaAs layer 37 and p-type cladding layer 35. Electrons are injected from the substrate electrode 41 into the n-type GaAs active layer 34 sequentially via the n-type GaAs substrate 31, multilayer reflecting mirror of n-type semiconductor 32 and n-type cladding layer 33.

[0005] When the prior art device 30 is used as a light amplifier device or particularly as a surface-emitting laser, the associated resonator comprises the mul-

tilayer reflecting mirror of n-type semiconductor 32 built in the device and an external reflecting mirror (not shown). Between the external reflecting mirror not shown and the antireflection coating 39 there is generally disposed a lens (not shown). It goes without saying that the antireflection coating 39 is used for reducing the resonator loss and obtaining the light gain. For the same reason, the layers 33 to 35, 37 and 38 are subjected to treatments such as for suppressing the impurity concentration etc. to a low degree so that the optical absorption loss can be lowered.

[0006] Figure 3 shows another prior art surface-type light amplifier device 50. This device is disclosed in Reference Literature 2: "High single-transverse-mode output from external-cavity surface-emitting laser diode" (M. A. Hadley, G. C. Wilson, K. Y. Lau and J. S. Smith, Appl. Phys. Lett., Vol. 63, No. 12, pp. 1607-1609, 1993) and comprises a GaAs substrate 51 not of n-type but of p-type, on which a multilayer reflecting mirror 52 of p-type semiconductor, a p-type multi-quantum well active region 53 and a multilayer reflecting mirror 55 of n-type semiconductor are deposited in the order mentioned. Voltage is applied between a substrate electrode 57 deposited on the bottom surface of the substrate 51 and a bonding pad 56 disposed on an insulating film 54 and brought into contact with the top peripheral surface of the multilayer reflecting mirror of n-type semiconductor 55 to inject an electric current (carriers) into the multi-quantum well active region 53, thereby obtaining excited light. Holes are injected from the side of the substrate electrode 57 into the multi-quantum well active layer 53 via the p-type GaAs substrate 51 and the multilayer reflecting mirror of p-type semiconductor 52, whereas electrons are injected thereto from the opposite side, i.e. from the bonding pad 56, via the multilayer reflecting mirror of n-type semiconductor 55.

[0007] This device 50 is, by nature, not a device for an external resonator. However, in the case that a resonator is composed only of the multilayer reflecting mirror of n-type semiconductor 55 and the multilayer reflecting mirror of p-type semiconductor 52 embedded in the device, it inevitably poses a substantial problem that the transverse mode does not become a single lobe when the diameter of the device is made large. In order to solve the problem it is necessary to provide an external reflecting mirror not shown. Single-lobe beams can be obtained by deliberately lowering the reflectivity of the multilayer reflecting mirror of n-type semiconductor 55, then providing a suitable reflecting mirror outside the device on the side of the multilayer reflecting mirror of n-type semiconductor 55, and adjusting the position of a lens disposed in an optical path toward the external reflecting mirror, for example. In any event, the resonator has a composite construction comprising a first resonator composed of the multilayer reflecting mirror of p-type semiconductor 52 and the multilayer reflecting mirror of n-type semiconductor 55 which are provided in the device and a second resonator composed of the

multilayer reflecting mirror of p-type semiconductor 52 and the external reflecting mirror.

[0008] In the device 30 shown in Figure 2, however, it is particularly difficult to obtain laser beams having a large diameter. This is because, if the effective area of the n-type GaAs active layer 34, i.e. the area coated with the antireflection coating 39 and actually contributing to oscillation, is made large for enlarging the device diameter, it will become impossible to uniformly inject holes into that area. This results solely from the fact that each of the p-type semiconductor layers 38, 37 and 35 has high electrical resistance. In order to inject holes into the neighborhood of the center of the effective area of the n-type GaAs active layer, it is necessary to cause the holes first to flow through the p-type semiconductor layers 38, 37 and 35 in the in-plane direction from the surface electrode 40 in contact with the peripheral edge of the antireflection coating 39 and then to be injected into the center of the n-type GaAs active layer 34. In the actual course of operation, however, this cannot be attained because the majority of holes are injected into the peripheral edge of the p-type GaAs contact layer 38 from the surface electrode 40 and then advance straightforward without being well spread laterally and reach the n-type GaAs active layer 34.

[0009] In order to actually secure the state of uniform hole injection into the n-type GaAs active layer 34 in the conventional device 30 fabricated in accordance with such structural principle, it is required to reduce the diameter of the effective area of the n-type GaAs active layer 34 to not more than tens of μm . That is to say, when a large output power is required, it is necessary to adopt a method of arraying a plurality of devices, resulting in sacrifices of singleness and uniformity of optical beams.

[0010] In the conventional device 50 shown in Figure 3, however, since holes can be injected from the substrate electrode 57 in surface contact with the back surface of the p-type GaAs substrate 51, the uniformity in the in-plane distribution of holes injected into the p-type multi-quantum well active region 53 will be satisfied. However, the serious problem is that the device has a composite resonator structure which cannot constitute a pure external resonator type surface light emitting laser and since the multilayer reflecting mirror of n-type semiconductor 55 incorporated in the device generally has a reflectance of not less than about 80%, the device is not suitable as a surface-type light amplifier device. In addition, due to the composite resonator structure, light pulses cannot be generated in the mode-locking operation.

[0011] Furthermore, since the multilayer reflecting mirror of n-type semiconductor 55 having a resistance lower than that of a p-type one is used for the sake of electron flow in the in-plane direction, the structure is designed for injecting electrons into the neighborhood of the center of the multi-quantum well active region 53. However, if the diameter of the multi-quantum well

active area 53 is set larger, the electrical resistance of the multilayer reflecting mirror of n-type semiconductor 55 cannot be ignored and unevenness in the current injection is induced. That is to say, for uniform current injection, the upper limit of the effective area of the multi-quantum well active area 53 is about $100\ \mu\text{m}$ in diameter though it is larger than that of the conventional device 30 shown in Figure 2. In particular, it is impossible to control the injection of holes into the active layer because the p-type electrode is the substrate electrode and an electric current is injected through the substrate.

[0012] The present invention has been proposed in view of the problems mentioned above, and its object is to provide a surface-type light amplifier device having at least a light amplification section including a structure of an active layer sandwiched between p-type and n-type cladding layers and emitting a light beam in the direction rising with a specific angle (generally, 90° as stated above) relative to the surface of a support substrate, wherein the amplification of a single and uniform light beam or, if required, a large-diameter light beam, and the laser oscillation can be attained.

[0013] The inventor believes that, in the final analysis, the various drawbacks of the conventional devices 30 and 50 shown in Figures 2 and 3 result from the presence per se of the n-type substrate 31 or the p-type substrate 51 forming the light amplification section contributing to the light amplification, namely, the multilayer structure including the semiconductor layers 32-35 and 37-38 in the device 30 shown in Figure 2 or the multilayer structure including the semiconductor layers 52, 53 and 55 in the device 50 shown in Figure 3.

[0014] It goes without saying that the substrate 31 or 51 is indispensable to the formation of the light amplification section and important even after the section has been formed as a support for securing the physical strength of the device. Insofar as the light amplifying function is concerned, however, the substrates 31 and 51 are rather unnecessary or obstructive. Since the substrates 31 and 51 generally have a large thickness of up to hundreds of μm , when a compound semiconductor substrate such as a GaAs substrate is employed, loss in passing amplified light therethrough is very large.

[0015] For this reason, both the conventional devices 30 and 50 shown in Figures 2 and 3 have a construction such that amplified light is not passed through the support substrates 31 and 51. This is the same in other conventional devices not touched upon here. In other words, various changes in construction for improving the characteristics of devices have heretofore had to be made on the major premise that light should not be passed through a substrate. This has brought about various restrictions. In the case of the conventional device 30 shown in Figure 2, for example, since the light emitted from the light amplification portion has to be emitted from the side of the p-type semiconductor layers 35, 37 and 38 opposite the side at which the n-type GaAs substrate (31) is present, this light emitting surface cannot

be covered by the electrode. As a result, an electric current has to be applied only through the peripheral edge of the p-type AlGaAs layer 37 to the p-type cladding layer and then to the n-type GaAs active layer 34 as described above, thereby inducing the aforementioned uneven hole injection and difficulty in achieving a large device diameter.

[0016] In the case of the conventional device 50 shown in Figure 3, the p-type GaAs substrate 51 is used in place of the n-type GaAs substrate, with the result that there is an advantage that the multilayer reflecting mirror of n-type semiconductor 55 can be disposed on the side opposite the side at which the substrate is present to achieve low resistance, but there are restrictions, such as requiring a composite resonance structure, resulting in the different drawbacks as described above. Disclosure of the Invention:

[0017] In view of the above, the present inventor, exploding the well-established concept, has conceived the idea of removing a base substrate used for fabricating a light amplification section after the fabrication of the light amplification section. However, since the light amplification section is an extremely thin structure, such mere removal of the base substrate would decrease the strength of the light amplification section resulting in physical distortion producing optical distortion, and would not allow the light amplification section to be put into practical use. Therefore, the present invention provides a structure having the light amplification section attached to a transparent support substrate that is separate from the base substrate used in fabricating the light amplification section and exhibits a low loss when a light beam passes therethrough.

[0018] With this device structure, a light beam amplified at the light amplification section can be passed through the transparent substrate. This means that there arises a degree of freedom in structural design. For example, an electrode through which holes are injected into a p-type semiconductor layer with relatively high resistance can be made large. Furthermore, even when a plurality of divided electrodes are formed and a light beam is prevented from being emitted from the side of these electrodes as in the specific embodiment of the present invention which will be described later, various improvements can be realized by enabling a light beam to be emitted via the transparent substrate provided on the opposite side of the electrodes with the active layer of the light amplification section therebetween.

[0019] The present invention also provides a surface-type light amplifier device, as a preferred unsophisticated embodiment satisfying the above fundamental conditions, having a light amplification section attached to a transparent substrate on the side on which an n-type semiconductor layer is present and having a plurality of divided electrodes provided on the side across an active layer opposite the n-type semiconductor layer for injecting holes into a p-type semiconductor layer.

[0020] In this surface-type light amplifier device, it is possible to uniformly inject holes into the p-type semiconductor layer having higher resistance than an n-type one. In addition, since the in-plane distribution of the carriers in the active layer can be controlled by controlling the amount of an electric current applied to the divided electrodes, it is possible to control the in-plane distribution to conform to the light intensity distribution in the fundamental mode having a single lobe.

[0021] The present invention provides, as a more concrete unsophisticated embodiment, a surface-type light amplifier device wherein an active layer in a light amplification section is sandwiched between an n-type semiconductor cladding layer that is an n-type semiconductor layer and a p-type semiconductor multilayer reflecting mirror that is a p-type semiconductor layer; the light amplification section is attached to a transparent substrate on the side of the n-type semiconductor cladding layer; a plurality of divided electrodes attain electric continuity through a p-type cap layer provided on the p-type semiconductor multilayer reflecting mirror; and an electrode forming electrical continuity relative to the n-type semiconductor cladding layer is connected to a wiring conductor provided on the transparent substrate.

[0022] The present invention further provides a method for fabricating a surface-type light amplifier device, which includes the steps of forming a light amplification section on a structural substrate for the formation of that section, attaching a different transparent substrate to the exposed surface of the light amplification section, and removing the structural substrate.

[0023] As an unsophisticated embodiment of the aforementioned fabrication method, the present invention provides a fabrication method comprising the steps of successively forming a p-type semiconductor layer, an active layer and an n-type semiconductor layer in the order mentioned on the structural substrate, attaching the transparent substrate to the exposed surface of the n-type semiconductor layer, and forming a plurality of divided electrodes on the surface of the p-type semiconductor layer exposed after the removal of the structural substrate, thereby attaining electrical continuity relative to the p-type semiconductor layer.

Brief Explanation of the Drawings:

[0024]

Figure 1(A) is a schematic view showing the configuration of one example of a surface-type light amplifier device according to the present invention, Figure 1(B) an explanatory view showing the process for fabricating the surface-type light amplifier device according to the present invention, Figure 2 a schematic view showing the configuration of one typical example of a conventional surface-type light amplifier device, and Figure 3 another typical exam-

ple of a conventional surface-type light amplifier device.

Best Mode for Working the Invention:

[0025] The present invention will now be described in detail with reference to the accompanying drawings.

[0026] Figure 1(A) shows a schematic configuration of one example of surface-type light amplifier device 10 fabricated in accordance with the present invention. In the present invention, a light amplification section 11 including a structure of sandwiching between p-type and n-type semiconductor layers 14 and 12 an active layer 13 that produces excited carriers is attached by means of a transparent adhesive 22 to a transparent substrate 21 that differs from a structural substrate on which the light amplification section 11 has been formed. That is to say, Figure 1(A) shows the state in which the structural substrate has been removed.

[0027] In the device 10 of the present invention thus configured, since a light beam produced at the light amplification section 11 can pass through the transparent substrate 21, the degree of freedom for constructional improvements in this section 11 increases. As described below, Figure 1(A) discloses the configuration in one preferred unsophisticated embodiment in accordance with the present invention. However, there can be provided other various surface-type light amplifier devices in accordance with the fundamental construction of the present invention. The material for the transparent substrate 21 may be glass, plastic, etc. having very high transparency with respect to the oscillation wavelength. From these materials can be easily obtained one having transmittance of 99 to 99.9% with respect to transmission beams in the optical wavelength range. It is noted, however, that the larger the thickness, the larger the transmission loss even when the transmission is markedly high. Generally, however, the thickness of the substrate that can physically support the light amplification section 11 thereon and can ensure a strength thereof large enough to prevent the section from distortion falls in the range of hundreds of μm to several mm, in which range the transparency is fully satisfactory.

[0028] As the transparent adhesive 22 commercially available polyimide etc. can be used. Since such an adhesive agent has sufficiently high transmittance and is used in the form of a thin film, there arises no problem in use. Surface levelling (optical precision) of the transparent substrate 21 and uniform application of the transparent adhesive 22 can be satisfactorily attained with ease using any prior art technology. In order to avoid diffused reflection on the surface of the transparent substrate 21, the interface between the transparent substrate 21 and the transparent adhesive 22 and the interface between the transparent adhesive 22 and the light amplification section 11, antireflection coatings 25, 24 and 23 are applied beforehand to these

faces. The antireflection coatings can be constituted of a double-layer laminate structure of TiO_2 and SiO_2 .

[0029] In the surface-type light amplifier device 10 of the present invention as shown in Figure 1(A), the concrete construction of the light amplification section 11 is as follows. Attached through the transparent adhesive 22 and antireflection coating 23 to the transparent substrate 21 is an n-type cladding layer 12 that can be constituted, for example, of an n-type $\text{Al}_x\text{Ga}_{1-x}\text{As}$ ($x = 0.3$) layer having a thickness of about $2\ \mu\text{m}$.

[0030] On that layer is formed an active layer 13 that is the principal part of the light amplification section 11 and is constituted of a non-doped GaAs layer having a thickness of about $0.5\ \mu\text{m}$.

[0031] On the active layer 13 is formed a p-type semiconductor multilayer reflecting mirror 14 that is a p-type semiconductor layer and is constituted, for example, of a periodical repetition laminate structure of an $\text{Al}_x\text{Ga}_{1-x}\text{As}$ ($x = 0.1$) layer and a p-type AlAs layer. Each layer has a considerably small thickness, and the total thickness of the structure is approximately several μm . However, since such a semiconductor multilayer reflecting mirror per se is well known, it can be produced in accordance with an optional prior art technology.

[0032] As will be understood from the fact that a semiconductor multilayer reflecting mirror 14 is used as a p-type semiconductor layer in the surface-type light amplifier device 10 shown in Figure 1(A), the device has a structure such that a light beam produced at the active layer 13 is reflected by the reflecting mirror 14 and is radiated to an exterior space through the n-type cladding layer 12 and transparent substrate 21. Therefore, unlike the conventional device of Figure 3 relying on the composite resonator structure constructed together with a resonator built in the device, it is possible to provide a complete unit of external reflecting mirror type light emitting laser, though not shown, by disposing a commercially available high-performance reflecting mirror, e.g. a planar dielectric multilayer reflecting mirror having reflectivity of not less than 99%, in an exterior space on an extension of the emission path of the light beam while interposing a suitable lens between the device and the mirror, if necessary.

[0033] In order to produce excited carriers in the active layer 13, it is required to inject an electric current into the active layer 13. The constitution for this requirement is as described hereinafter, wherein a particularly effective arrangement is adopted.

[0034] On the p-type semiconductor multilayer reflecting mirror 14 is formed a p-type GaAs cap layer 15 having a thickness of $3000\ \text{\AA}$ and doped with high-concentration Zn. The particularly effective arrangement is that a plurality of divided electrodes 16 for hole injection are provided on the cap layer. In the embodiment shown in Figure 1, the electrodes 16 comprise a disc-shaped electrode at the center and a plurality of ring-like electrodes concentric with the center electrode and arranged at prescribed intervals. With this struc-

ture, the in-plane distribution of holes injected from the electrodes into the active layer via the p-type cap layer 15 and p-type semiconductor multilayer reflecting mirror 14 can be controlled to be uniform by controlling voltage applied to the individual electrodes, and active control can be made in the operation of the device.

[0035] That is to say, while, in the conventional device, the hole flow through a very thin p-type semiconductor layer with high resistance into an active layer is deviated, resulting in an obstacle to uniform hole injection, the structure shown in the drawing makes it possible not only to inject holes uniformly into the active layer 13, but also to control the injection current distribution more positively to obtain a gain distribution suitable for a light distribution of an oscillation mode etc. When a light distribution of the fundamental mode having a single lobe is to be obtained, for example, the amounts of an electric current to be injected into the electrodes are set higher toward the central electrode. It is further possible to obtain a stable and highly precise lasing light, a lasing light beam of a desired far-field pattern, etc.

[0036] The divided electrodes 16 are not limited to the concentric ones as shown in Figure 1, but can optionally be selected from a pattern of strips arranged in parallel, a predetermined plane pattern of dots each of circular, rectangular shape or the like, and other patterns.

[0037] On the other hand, the electrode for injecting electrons into the n-type semiconductor layer 12 with relatively low resistance can be formed more simply. In Figure 1, a ring-like contact layer 17 in contact with the bottom surface of the n-type semiconductor layer (cladding layer) 12 is formed along the outer periphery of the light amplification section 11 formed into a predetermined solid (cylindrical shape in the drawing) as a whole, and an electrode 18 of an alloy of AuGe or the like formed on the contact layer is electrically connected via an electrode connecting member 19 to a wiring conductor 20 formed on the surface of the transparent substrate 12 around the light amplification section 11. The contact layer 17 has a thickness of about 1000 Å and can be constituted of an n-type GaAs layer, for example. The electrode connecting member 19 can be constituted of a solder of In or AuSn. The wiring conductor 20 can be constituted of Au or the like.

[0038] As mentioned briefly earlier, when the device shown in the drawing is used as an external resonator type surface light emitting laser, a resonator can be constituted of the p-type semiconductor multilayer reflecting mirror 14, and an external reflecting mirror and a lens which are not shown in the drawing. The lens is disposed in an exterior space opposite the active layer 13 on the side of the transparent substrate 21, and the external reflecting mirror is disposed on an extension of a line connecting the transparent substrate 21 and the lens. That is to say, the surface-type device 10 is disposed so that the substrate 21 faces the external reflecting mirror with the lens intervening therebetween.

The device, lens and external reflecting mirror are placed on a fine-motion table capable of optical adjustment. The external reflecting mirror is a planar-type reflecting mirror that reflects light having an emission wavelength at sufficiently high reflectivity, e.g. a dielectric multilayer reflecting mirror having reflectivity of not less than 99%.

[0039] The light reflected by the p-type semiconductor multilayer reflecting mirror 14 is amplified again in the area of the active layer 13, collimated by the lens, reflected again by the external reflecting mirror, and returned to the active layer 13 and to the p-type semiconductor reflecting mirror 14. This sequence is repeated to give rise to laser oscillation. At this time, an adjustment mechanism such as the fine-motion table is used to optically adjust the device, lens and external reflecting mirror so as to reduce resonator loss. The distance between the external reflecting mirror and the device, i.e. the external resonator length, is made sufficiently small. This is for avoiding the influence of vibration etc.

[0040] An electric current is applied to the divided electrodes 16 so as to be matched with the light intensity distribution in the fundamental mode of single lobe, namely so that it flows mostly through the center electrode. By this, it is possible to realize an external resonator-type surface light emission laser with a large output. In addition, the electric current applied to the divided electrodes 16 is suitably modulated for the round-trip time of light passing through the length of the external resonator, e.g. modulated at 1 GHz where the round-trip time of light is 1 ns, with the result that an active mode-locking operation is assumed to enable generation of light pulses.

[0041] The surface-type light amplifier devices according to the present invention, inclusive of the one designated by 10 shown in Figure 1(A), can be fabricated by various methods. However, a desirable process for fabricating the device 10 shown in Figure 1(A) can be exemplified in Figure 1(B). As shown at step 101, a commercially available GaAs substrate having a thickness of approximately 400 μm is used as a substrate for constructing a light amplification section, and on the substrate is formed an $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layer ($x = 0.6$) having a thickness of around 3000 Å that functions as an etching prevention layer in a subsequent etching step.

[0042] On the etching prevention layer is constructed the light amplification section 11 of Figure 1(A), in an inverted state, as shown at step 102. Therefore, the divided electrodes 16 cannot be formed at this step. To be specific, the p-type cap layer 15, p-type semiconductor multilayer reflecting mirror 14, active layer 13, n-type cladding layer 12 and contact layer 17 are successively deposited in the order mentioned. The light amplification section 11 in a predetermined three-dimensional shape such as a columnar shape is formed by using a deposition technique and lithography or a like

technique together. Lithography is similarly utilized to remove part of the contact layer 17 present at the beam passageway and exhibiting a large optical loss. The antireflection coating 23 is further formed when found necessary by using a coating technique and lithography together.

[0043] As shown at subsequent step 103, to the side of the n-type cladding layer 12 exposed to the outside, namely to the side of the antireflection coating 23 when formed, is attached a transparent substrate 21, such as of glass, having a highly flat surface via the transparent adhesive of polyimide or the like applied uniformly onto that side.

[0044] The physical strength of the light amplification section 11 is thus secured beforehand so as not to induce any physical or optical distortion. The thus constructed structure is then subjected to a substrate polishing step. Since the substrate is as thick as about 400 μm as shown above, it is polished as shown at step 104 using a mechanical polishing method so that the resultant thickness is approximately 30 to 100 μm .

[0045] After the substrate has been polished to have such a thickness, it is etched at an appropriate temperature with a suitable solution, e.g. at about 20°C with a mixed solution of one part of ammonia and twenty parts of a hydrogen peroxide solution as shown at step 105. When the aforementioned $\text{Al}_x\text{Ga}_{1-x}\text{As}$ layer ($x = 0.6$) has been formed as an etching prevention layer on the substrate, the substrate can be etched up to the etching prevention layer at high speed of about 20 $\mu\text{m}/\text{min}$ without strictly managing the etching time, and it is possible to stop the etching. As the etching prevention layer, an AlAs layer or the like can also be used.

[0046] As shown at subsequent step 106, the etching prevention layer is removed by etching it with a mixed solution of phosphoric acid, a hydrogen peroxide solution and water ($\text{H}_3\text{PO}_4:\text{H}_2\text{O}_2:\text{H}_2\text{O} = 3:1:50$) at about 20°C. Since this etching is effected at a speed of about 1000 $\text{\AA}/\text{min}$., the etching prevention layer can be easily removed under time management.

[0047] When the surface of the p-type cap layer 15 of the light amplification section 11 has been exposed to the outside, a predetermined number of divided electrodes 16 for injection of holes are formed in a predetermined pattern of arrangement as shown at step 107 by depositing an alloy of AuZn or the like onto the entire surface of the cap layer and then effecting lithography, or by using a printing technique after a predetermined pattern.

[0048] The electrical structural parts 17 to 20 to be formed on the n-type semiconductor layer (cladding layer) 12 not described in detail herein above can be fabricated by a known method comprising suitable steps.

[0049] Although a preferred embodiment has been described in the foregoing, any modification can be made therein insofar as it does not depart from the gist of the present invention. In addition to AlGaAs used as

the material for the light amplification section 11, InGaAsP, GaN, etc. that similarly fall under Group III-V semiconductors and photo-semiconductor materials such as ZnSe that fall under Group II-VI semiconductors can also be used.

Industrial Applicability:

[0050] According to the present invention, since the substrate used for constructing a light amplification section can be removed and since light can be transmitted through a transparent substrate, the degree of freedom with respect to improvements in the light amplification section increases to a great extent.

[0051] When an electrode for injection of holes to be provided on the side of a p-type semiconductor layer is composed of a plurality of divided electrodes in accordance with the preferred embodiment of the present invention, the amount of an electric current applied to each of the divided electrodes can be controlled to enable carrier distribution in an active layer to be actively controlled. As a consequence, it is possible to inject carriers in accordance with light intensity distribution in the fundamental mode having a single lobe when fabricating an external resonator-type surface light emitting laser to enable the operation in the fundamental mode to be stabilized.

[0052] Furthermore, when the side of the transparent substrate is used as a light emitting side, the p-type semiconductor functions as a multilayer reflecting mirror. For this reason, due to the optical stability of the p-type multilayer reflecting mirror obtained by fixing it to the transparent substrate, the range of effective active region can be enlarged to materialize a current injection-type external resonator-type surface light emitting laser with a large output, a light beam reflecting-type amplifier with a large diameter, etc. It is fully possible to achieve generation of a light beam having a diameter of several hundreds of μm or more. It goes without saying that since the surface-type light amplifier device of the present invention is of a current injection type, an external-resonator active-mode locking surface light emitting laser can be materialized. In this case, a high output of light pulses can be generated.

[0053] In Figure 1(B):

101: Formation of an etching prevention layer ($\text{Al}_{0.6}\text{Ga}_{0.4}\text{As}$) on a structural substrate (GaAs) for forming a light amplification section.

102: Formation of the light amplification section on the etching prevention layer.

103: Attachment of the light amplification section to a transparent substrate.

104: Polishing of the structural substrate into a predetermined thickness.

105: Etching of the remainder of the structural substrate.

106: Removal of the etching prevention layer by

etching.

107: Formation of divided electrodes.

Claims

1. A surface-type light amplifier device (10) having a light amplification section (11) that includes a structure of an active layer (13) sandwiched between a p-type semiconductor layer (14) and an n-type semiconductor layer (12) for producing excited carriers and emits a light beam in a direction rising with a specific angle relative to a surface of a support substrate,

said surface-type light amplifier device being characterized in that said light amplification section is attached to a transparent substrate (21) different from a substrate for fabricating said light amplification section and that a light beam emitted from said active layer (13) is transmitted through said transparent substrate.

2. A surface-type light amplifier device according to claim 1, characterized in that said light amplification section is attached to the transparent substrate on a side of said n-type semiconductor layer, and that an electrode for injection of holes formed on said p-type semiconductor layer opposite said n-type semiconductor layer with said active layer intervening therebetween comprises a plurality of divided electrodes (16).

3. A surface-type light amplifier device according to claim 2, characterized in that said active layer included in said light amplification section is sandwiched between said n-type semiconductor layer serving as an n-type semiconductor cladding layer and said p-type semiconductor layer serving as a p-type semiconductor multilayer reflecting mirror, that said light amplification section is attached to said transparent substrate on a side of said n-type semiconductor cladding layer, that said plurality of divided electrodes (16) attain electric continuity through said p-type cap layer (15) provided on said p-type semiconductor multilayer reflecting mirror (14), and that an electrode (18) forming electrical continuity relative to the n-type semiconductor cladding layer is connected to a wiring conductor (20) provided on said transparent substrate.

4. A method for fabricating a surface-type light amplifier device (10) having a light amplification section (11) that includes a structure of an active layer (13) sandwiched between a p-type semiconductor layer (14) and an n-type semiconductor layer (12) for producing excited carriers and emits a light beam in a direction rising with a specific angle relative to a surface of a support substrate, said method com-

prising the steps of:

forming said light amplification section (11) on a structural substrate for forming said light amplification section;
attaching an exposed surface of said light amplification section to a separate transparent substrate (21); and
removing said structural substrate.

5. A method for fabricating a surface-type light amplifier device according to claim 4, characterized in:

that said p-type semiconductor layer, said active layer and said n-type semiconductor layer are formed on said structural substrate in the order mentioned;
that said transparent substrate is attached to an exposed surface of said n-type semiconductor layer; and
that a surface of said p-type semiconductor layer (14) exposed after the removal of said structural substrate is formed with a plurality of divided electrodes (16) for forming electrical continuity relative to said p-type semiconductor layer.

FIG. 1 (A)

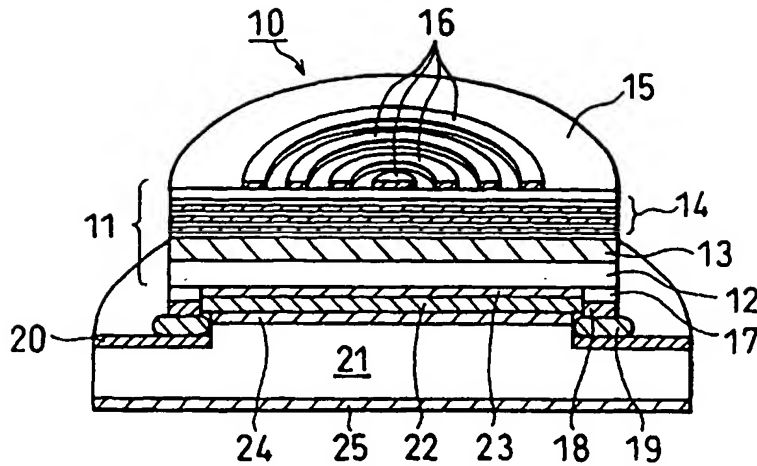


FIG. 1 (B)

FORMATION OF AN ETCHING PREVENTION LAYER ($\text{Al}_{0.6}\text{Ga}_{0.4}\text{As}$) ON A STRUCTURAL SUBSTRATE (GaAs) FOR FORMING A LIGHT AMPLIFICATION SECTION.

FORMATION OF THE LIGHT AMPLIFICATION SECTION ON THE ETCHING PREVENTION LAYER.

ATTACHMENT OF THE LIGHT AMPLIFICATION SECTION TO A TRANSPARENT SUBSTRATE.

POLISHING OF THE STRUCTURAL SUBSTRATE INTO A PREDETERMINED THICKNESS.

ETCHING OF THE REMAINDER OF THE STRUCTURAL SUBSTRATE.

REMOVAL OF THE ETCHING PREVENTION LAYER BY ETCHING.

FORMATION OF DIVIDED ELECTRODES.

FIG. 2

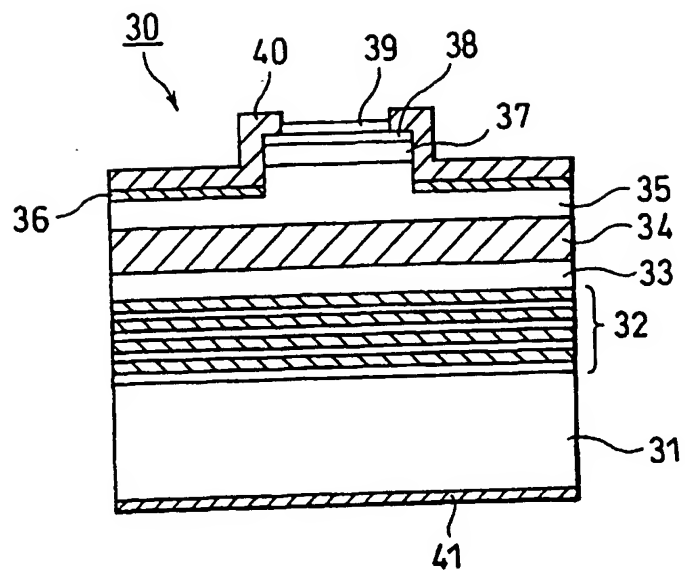
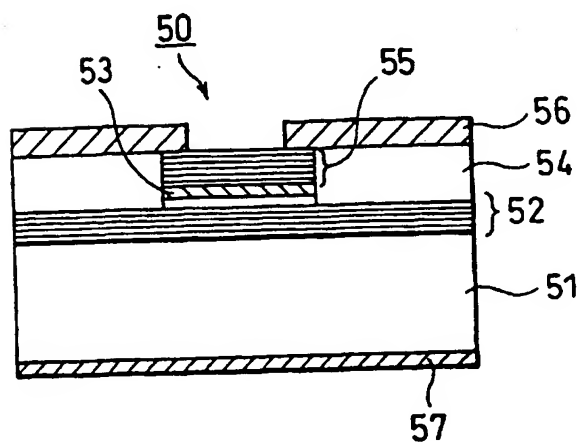


FIG. 3



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP99/01689

A. CLASSIFICATION OF SUBJECT MATTER Int.Cl. ⁶ H01S3/18 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) Int.Cl. ⁶ H01S3/18		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Jitsuyo Shinan Koho 1922-1996 Toroku Jitsuyo Shinan Koho 1994-1999 Kokai Jitsuyo Shinan Koho 1971-1999 Jitsuyo Shinan Toroku Koho 1996-1999		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 10-107389, A (Alcatel Alsthom Compagnie d'Electricite), 24 April, 1998 (24. 04. 98), Par. Nos. [0070] to [0078] ; Figs. 3 to 8 & EP, 829934, A	1-5
Y	JP, 2-45994, A (Sanyo Electric Co., Ltd.), 15 February, 1990 (15. 02. 90), Page 2, lower right column, line 12 to page 3, line 10 ; Fig. 1 (Family: none)	1-5
Y	JP, 09-283860, A (Canon Inc.), 31 October, 1997 (31. 10. 97), Par. Nos. [0035], [0036] ; Fig. 2 (Family: none)	3
Y	JP, 9-260763, A (Olympus Optical Co., Ltd.), 3 October, 1997 (03. 10. 97), Par. Nos. [0035] to [0038] ; Fig. 8 (Family: none)	2, 5
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search 23 June, 1999 (23. 06. 99)		Date of mailing of the international search report 6 July, 1999 (06. 07. 99)
Name and mailing address of the ISA/ Japanese Patent Office		Authorized officer
Facsimile No.		Telephone No.

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP99/01689

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 9-260765, A (Olympus Optical Co., Ltd.), 3 October, 1997 (03. 10. 97), Par. Nos. [0023] to [0028] ; Fig. 1 (Family: none)	2, 5
A	JP, 7-263806, A (Fujitsu Ltd.), 13 October, 1995 (13. 10. 95), Full text ; Figs. 1 to 5 (Family: none)	1
A	JP, 8-321660, A (Nichia Chemical Industries, Ltd.), 3 December, 1996 (03. 12. 96), Full text ; Fig. 1 (Family: none)	1
A	JP, 9-129984, A (Toshiba Corp.), 16 May, 1997 (16. 05. 97), Full text ; Fig. 1 & US, 5780873, A	1, 4
A	JP, 64-8690, A (Sharp Corp.), 12 January, 1989 (12. 01. 89), Full text ; Fig. 1 (Family: none)	1

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